

IN THE SPECIFICATION

The present application is related to the following listed seven applications:

Serial No. 10/016,346 [(RPS920010125US1)] entitled "Field Programmable Network Processor and Method for Customizing a Network Processor;" Serial No. 10/016,449 [(RPS920010127US1)], entitled "Method and System for Use of a Field Programmable Gate Array (FPGA) Function Within an Application Specific Integrated Circuit (ASIC) to Enable Creation of a Debugger Client Within the ASIC;" Serial No. 10/016,448 [(RPS 920010128US1)], entitled "Method and System for Use of a Field Programmable Function Within an Application Specific Integrated Circuit (ASIC) To Access Internal Signals for External Observation and Control;" Serial No. 10/015,922 [(RPS920010129US1)], entitled "Method and System for Use of a Field Programmable Interconnect Within an ASIC for Configuring the ASIC;" Serial No. 10/015,920 [(RPS920010130US1)], entitled "Method and System for Use of a Field Programmable Function Within a Chip to Enable Configurable I/O Signal Timing Characteristics;" Serial No. 10/015,923 [(RPS920010131US1)], entitled "Method and System for Use of a Field Programmable Function Within a Standard Cell Chip for Repair of Logic Circuits;" and Serial No. 10/015,921 [(RPS920010132US1)], entitled "Method and System for Use of a Field Programmable Gate Array (FPGA) Cell for Controlling Access to On-Chip Functions of a System on a Chip (SOC) Integrated Circuit;" assigned to the assignee of the present application, and filed on the same date.